

09/597 161

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTORNEY DOCKET NO. 025311-0105

Applicant: Ichiro OKABE et al.

Title: METHOD OF FORMING A FINE PATTERN USING A
SILICON-OXIDE-BASED FILM, SEMICONDUCTOR DEVICE
WITH A SILICON-OXIDE-BASED FILM AND METHOD OF
MANUFACTURE THEREOF

Patent. No.: 6,992,013

Issue Date: 01/31/2006

Examiner: Jose R. Diaz

Art Unit: 2815

Certificate
MAR 30 2006
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION FOR
PURSUANT TO 37 C.F.R. § 1.323

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed, in duplicate, is a Certificate of Correction, Form PTO-SB/44, for United States Patent Number 6,992,013 issued January 31, 2006.

Applicants submit that the noted errors do not constitute new matter, and correction thereof would not require reexamination.

Pursuant to 37 C.F.R. §1.323, Applicants request that the enclosed Certificate of Correction be approved.

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MAR 30 2006

Since at least one of the noted errors is not the fault of the Patent Office, a credit card payment form is enclosed in payment of the required fee of \$100.00. The Commissioner is hereby authorized to charge any additional fees which may be required for this Request, to Deposit Account No. 19-0741.

Respectfully submitted,

March 27, 2006


Date

FOLEY & LARDNER LLP

Customer Number: 22428

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William T. Ellis
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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 6,992,013
APPLICATION NO. : 09/597,161
DATED : 01/31/2006
INVENTOR(S) : Ichiro OKABE; Hiroki ARAI

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

ON THE FRONT OF PATENT

(73) Assignees: Delete "Semiconductor Leading Edge Technologies, Inc." and replace it with --Sharp Kabushiki Kaisha--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Washington Harbour
3000 K Street, N.W., Suite 500
Washington, D.C. 20007-5143

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